



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Pre Patent Application of

YAMAGATA et al.

Atty. Ref.: 925-286; Confirmation No.

Appl. No. 10/814,241

TC/A.U. 2812

Filed: April 1, 2004

Examiner: Booth, Richard A.

For: METHOD FOR MANUFACTURING NONVOLATILE SEMICONDUCTOR MEMORY  
WITH NARROW VARIATION IN THRESHOLD VOLTAGES OF MEMORY CELLS

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August 17, 2005

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**AMENDMENT IN RCE**

Responsive to the Official Actions dated July 21, 2005 and March 17, 2005 (for which petition is hereby made for a two month extension of time), please amend the above-identified application as follows:

08/18/2005 JAD001 00000096 10814241

02 FC:1252

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